

[FPGA 2026] Decision Notification for Submission #111

 **FPGA 2026 HotCRP** 2025-11-22 16:30
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Dear Author(s),

Congratulations! Your paper

* Title: MegaTurbo: A Scalable FPGA-based Engine for MegaFlow Classifier
in Open vSwitch

has been ****accepted**** at the 34th ACM/SIGDA International Symposium on
Field-Programmable Gate Arrays (FPGA 2026).

We are excited to have your paper in this year's program and to have you
present it to the FPGA community in Seaside, California, in February.

There's a long list of important instructions that you need to be aware
of and some critical deadlines, so please read carefully the rest of
this email.

These instructions and all future author guidelines will be posted on
the "Instructions to Authors" page
(<https://www.isfpga.org/instructions-to-authors/>).

Congratulations once again, and we look forward to seeing you in
Seaside.

Best regards,
Grace Zgheib
FPGA'26 Program Chair

Important Deadlines:

- November 24: Artifact Evaluation Registration (for long papers)
- December 15: Author registration
- December 18: Camera-ready and rights review form submissions

I- Camera-Ready Submission (due December 18th AoE)